



- DR Data Ready Line** normally +5V, will go low when the DC1 has data to transmit.
- DO Data Out** this is the data output line, which will contain valid data 1 or 0 while being clocked.
- XTAL1,XTAL2** Connect to 6mhz crystal, parallel resonance, LC circuit, or single phase clock.
- CI Clock Input** this is the clock input line, which will receive the “clocks” from your processor.
- BD Barcode Input** TTL level barcode input signal.
- SW1 Code39 Enable** Connect to +5VDC to enable Code39, connect to GND to disable Code39.
- SW2 I2/5 Enable** Connect to +5VDC to enable I2/5, connect to GND to disable I2/5.
- SW3 I2/5 Check Digit** Connect to +5VDC to enable I2/5 check digit checking. Connect to GND to disable check digit checking. If check digit enable is ON, only I2/5 barcodes with a proper check digit will be read.
- SW4 Bad Read** Connect to +5VDC for “bad read” message, connect to GND for no “bad read” message. If connected to +5VDC, the “bad read” message is ON, and whenever a barcode is scanned which does not decode, the message <count> <11111111> <Irc> is sent. This includes I2/5 barcodes which are properly read, but do not have a valid check digit (if check digit enable is on).
- Pins 8,9,10** Connect these pins to +5VDC using a 10k resistor.